

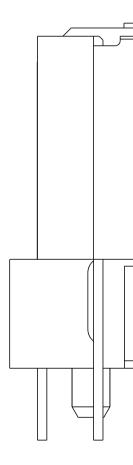
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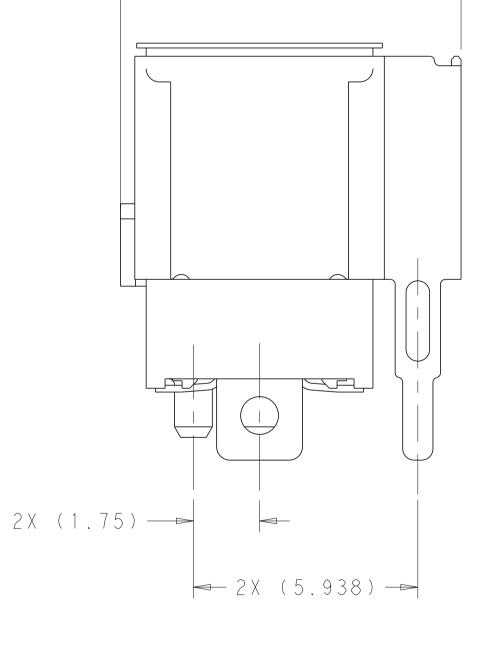
- ∠1 HOUSING LCP, UL94V-0, BLACK. CONTACT OVERMOLDS - LCP, UL94V-0, BLACK. SHELL, CONTACTS, HOLD DOWNS - COPPER ALLOY. PICK ÁND PLACE TAPE - POLYIMIDE FILM.
- 2 CONTACTS GOLD PLATE ON MATING SURFACES, TIN PLATE ON SOLDER FEET. HOLD DOWNS - TIN PLATE.

SHELL- NICKEL PLATE, TIN PLATE ON HOLD DOWNS.

- 3 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.
- 4. MINIMUM HOST PCB THICKNESS: 1.5.
- 5 SEE MSA SPECIFICATION FOR ADDITIONAL PADDLE CARD LAYOUTS COMPATABLE WITH THIS RECEPTACLE AND FOR OPTIONAL SPLIT CONTACT PAD LAYOUTS FOR THE PADDLE CARD. SPECIFICATION PINOUT MAY ALSO DESIGNATE PAD SEQUENCE DIFFERENT FROM ILLUSTRATION.



THIS DRAWING IS A DIMENSIONS: mm ATERIAL 1



— 9.1 MAX — —

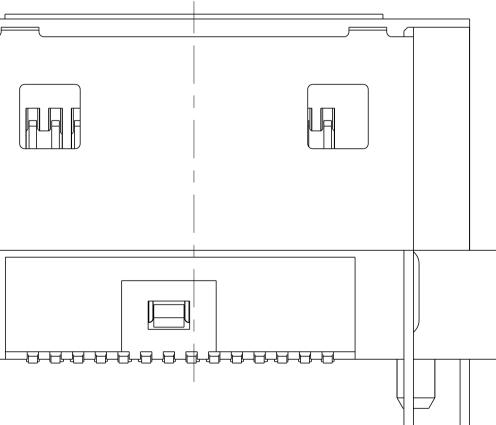
		REVISIONS			
Р	LTR	DESCRIPTION	DATE	DWN	APVD
	A	RELEASED PER ECO-19-013357	29AUG2019	CJV	JW
	В	REVISED PER ECO-19-014774	26SEP2019	CJV	JW
	B 1	REVISED PER ECN-22-180034	180CT2022	ТМ	DZ

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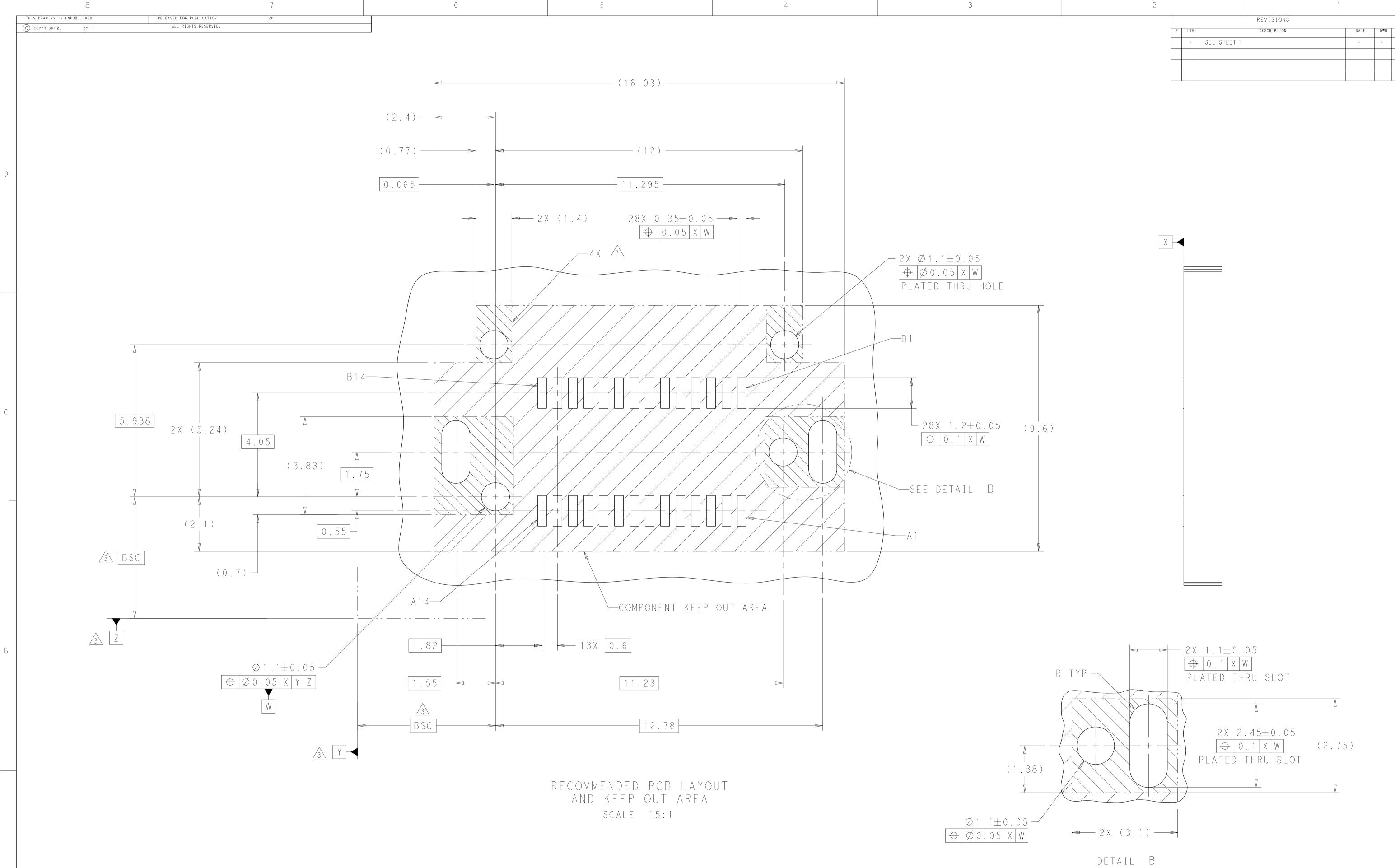
6 POSITIONS DESIGNATED AS "SIGNAL" ARE REQUIRED LOCATIONS FOR HIGH SPEED DIFFERENTIAL PAIR SIGNALING. THESE LOCATIONS MAY ALSO BE USED FOR SUPPORTING SIDEBAND SIGNALS OR OTHER UTILITY PURPOSES. POSITIONS DESIGNATED AS "GROUND" ARE REQUIRED WHEN SUPPORTING HIGH SPEED DIFFERENTIAL SIGNALS. THESE LOCATIONS MAY ALSO BE USED FOR SIDEBAND SIGNALS OR OTHER UTILITY PURPOSES.

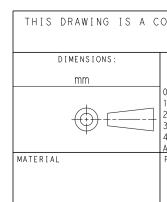
RECOMMENDED COMPONENT AND TRACE KEEP OUT AREA. EACH EDGE 0.15 MIN FROM EDGE OF HOLE.

- B PART NUMBER AND DATE CODE MARKED IN APPROXIMATE AREA SHOWN.
- 9. TAPE AND REEL PACKAGED FOR PICK AND PLACE SURFACE MOUNT PROCESSING. POCKET TAPE PITCH = 20.



		0.76µm	Au	2	200	Ĩ	2354	531-	3	
		0.38µm	Au	1	00	Ĺ	2354	531-	2	
	F	LASH Au,	/PdNi		50	Ĩ	2354	531-	1	
		PLATIN	NG		TING CLES	P A	₹R T	NUMB	ER	A
CONTROLLED DOC		M.SHIRK	APR2019 APR2019		E <i>T</i>	E te	Connec	:tivity		
TOLERANCES U OTHERWISE SPEC	NLESS :1FIED: 		APR2019 NAMI 	RECEPTA 28 POSI -		SEMBLY, SLIVER 7			STRICTED TO	
2		- CUSTOMER DRAWI	NG			SCALE 10:1	SHEET 1	OF 3	^{rev} B1	





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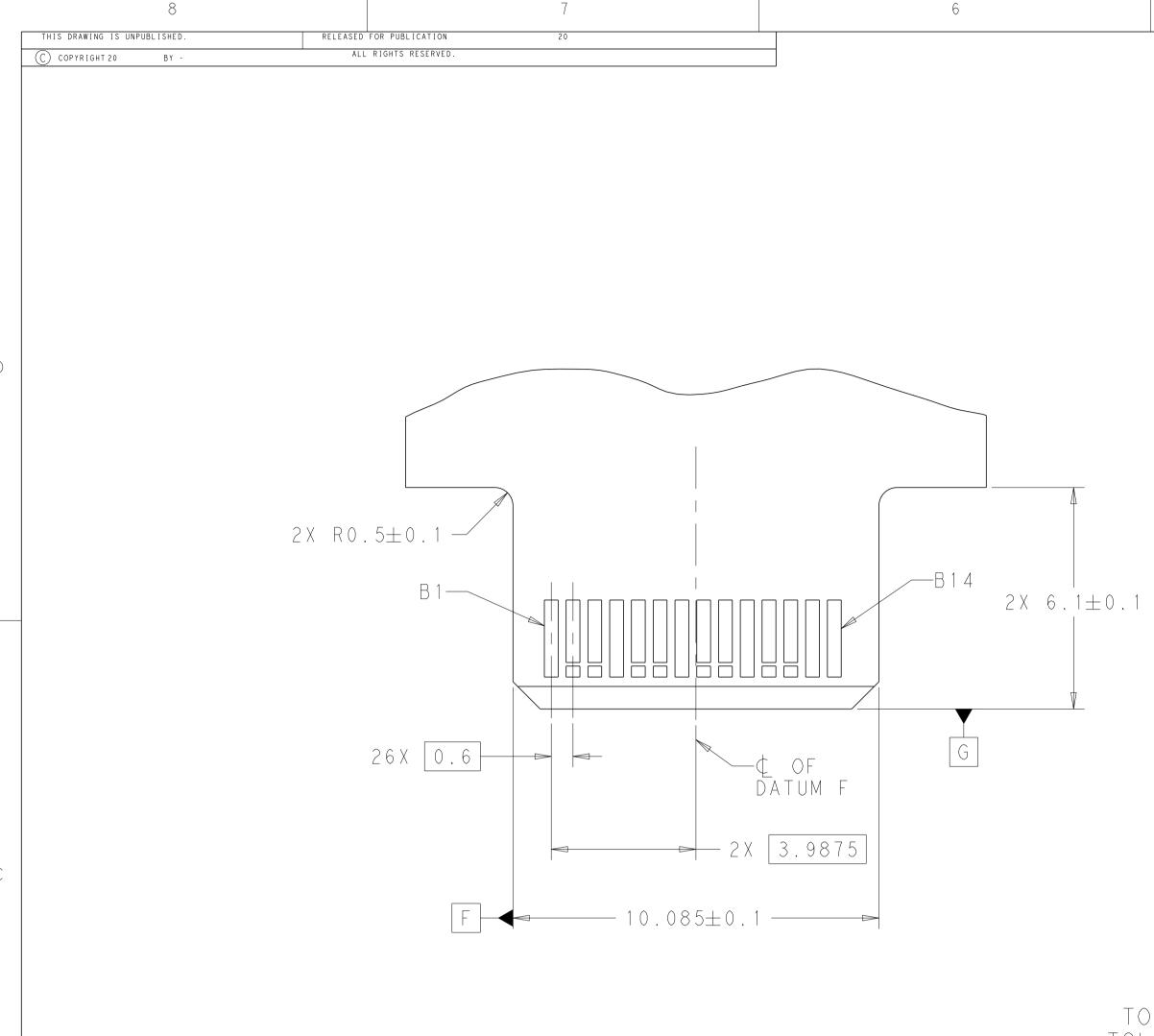
JUALE Z	υ. Ι	
CONTROLLED DOCUMENT.	DWN 08APR2019 <u>М. SHIRK</u> снк 08APR2019 J.WESTMAN	TE Connectivity
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±- 2 PLC ±- 3 PLC ±- 3 PLC ±-	APVD 08APR2019 J.WESTMAN PRODUCT SPEC 108-130021 APPLICATION SPEC	RECEPTACLE ASSEMBLY, VERTICAL, 28 POSITION, SLIVER 2.0 -
4 PLC ±- ANGLES ±- FINISH	114-130015 weight	SIZE CAGE CODE DRAWING NO RESTRICTED TO A 1 0 0 7 9 C=2354531 -
	CUSTOMER DRAWING	SCALE 10:1 SHEET 2 OF 3 REV B 1

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SCALE	20:1

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			REVISIONS			
P	LTR		DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1		-	-	-

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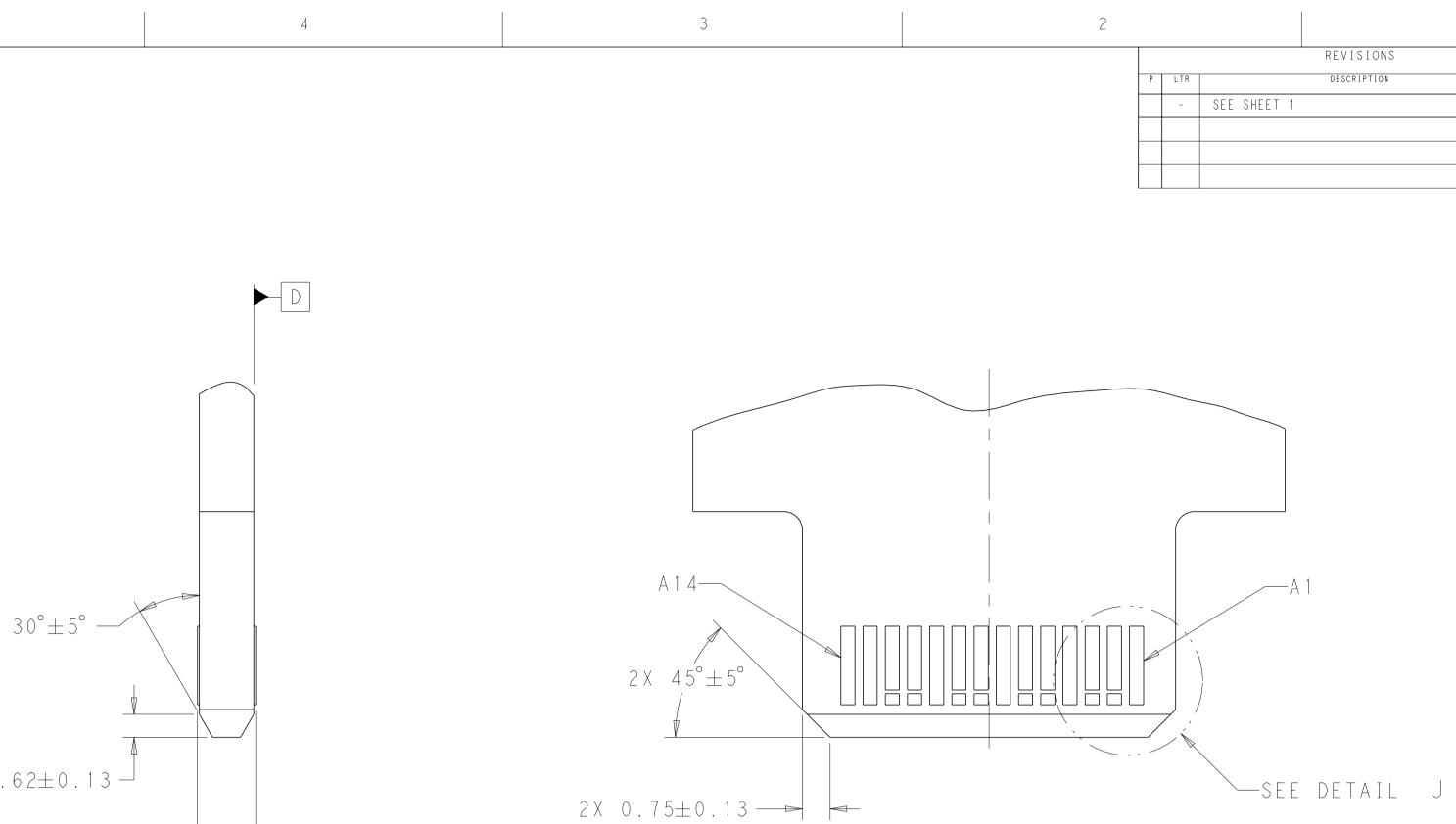
CONTACT NUMBER	SIDE A	SIDE B
1	GROUND	GROUND
2	SIGNAL	SIGNAL
3	SIGNAL	SIGNAL
4	GROUND	GROUND
5	SIGNAL	SIGNAL
6	SIGNAL	SIGNAL
7	GROUND	GROUND
8	SIGNAL	SIGNAL
9	SIGNAL	SIGNAL
1 0	GROUND	GROUND
1 1	SIGNAL	SIGNAL
1 2	SIGNAL	SIGNAL
1 3	GROUND	GROUND
14	GROUND	GROUND

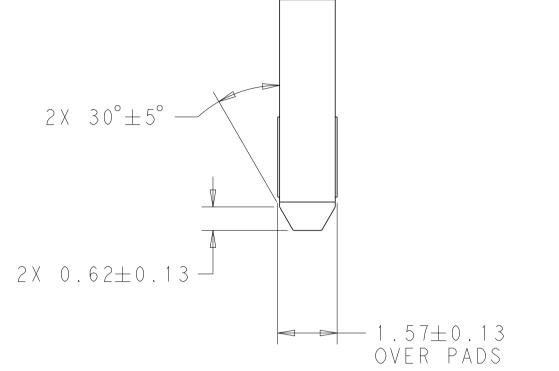
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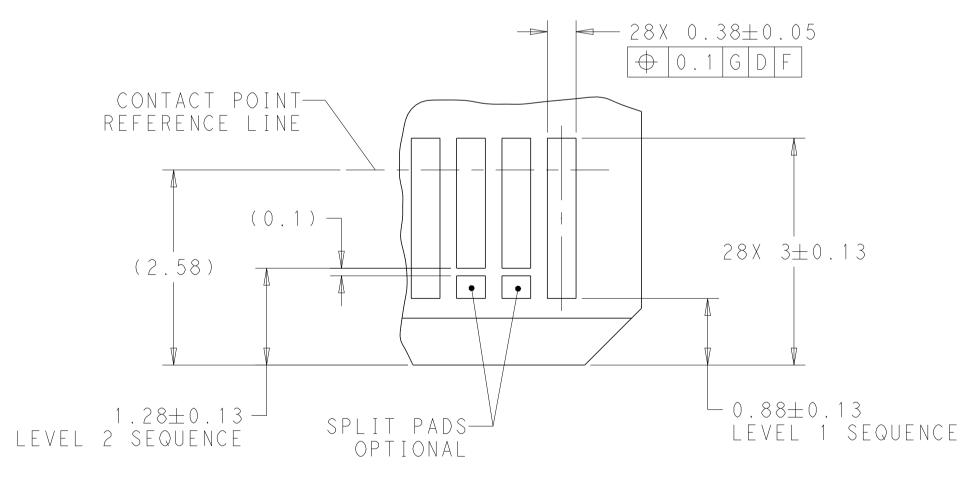
CONNECTOR CONTACT IDENTIFICATION 56

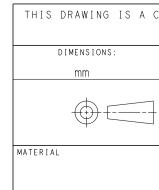




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RECOMMENDED PCB OUTLINE DIMENSIONS. TOLERANCE VALUES ARE CRITICAL. PLEASE BE SURE TO DESIGNATE TOLERANCES TO PCB SUPPLIER TO ENSURE OPTIMIZED FUNCTIONALITY.





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REVISIONS				
LTR	DESCRIPTION	DATE	DWN	APVD
-	SEE SHEET 1	-	-	-
		LTR DESCRIPTION	LTR DESCRIPTION DATE	LTR DESCRIPTION DATE DWN

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DETAIL J SCALE 20:1

С	ONTROLLED DOCUMENT.	DWN 08APR2019 М.SHIRK Снк 08APR2019	TE Connectivity
	TOLERANCES UNLESS OTHERWISE SPECIFIED:	J.WESTMAN Apvd 08APR2019 J.WESTMAN	NAME RECEPTACLE ASSEMBLY, VERTICAL,
7	0 PLC ±- 1 PLC ±- 2 PLC ±- 3 PLC ±-	PRODUCT SPEC 108-130021 APPLICATION SPEC	28 POSITION, SLIVER 2.0
	4 PLC ±- ANGLES ±- FINISH	114-130015	SIZE CAGE CODE DRAWING NO
	T INI SH	CUSTOMER DRAWING	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$
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